

Notice of References Cited	Application/Control No. 10/560,450		Applicant(s)/Patent Under Reexamination KESSELS ET AL.	
	Examiner JARED I. RUTZ		Art Unit 2187	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,775,717	08-2004	Tang et al.	710/22
*	B	US-6,847,650	01-2005	Stone et al.	370/431
*	C	US-2002/0083287	06-2002	Zumkehr et al.	711/167
*	D	US-2004/0003194	01-2004	Bodas et al.	711/167
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	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Implementing a 128Kx32 Dual-Port RAM Using the FLASH370, December 1995, Cypress Semiconductor Corporation, pages 1-11
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.